IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Jeffery W. Janzen et al.

Serial No.: Unassigned

Filed: Herewith

For: Memory Modules Having Accurate Operating Current Values Stored

Thereon and Methods for Fabricating and Implementing Such Devices

Group Art Unit: Unassigned

Examiner:

Unassigned

Atty. Docket: MICS:0103 FLE/MAN

(02-1327)

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313

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I hereby certify that this correspondence is being transmitted by facsimile to the United States Patent and Trademark Office in accordance with 37 C.F.R. 1.6(d) or is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313, on the date below:

April 1, 2004

Date

Robert A. Manware

Sir:

INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 C.F.R. §§ 1.97(b)(1) AND 1.98

In compliance with the duty of disclosure under 37 C.F.R. § 1.56(a), Applicants respectfully request that this Information Disclosure Statement be entered and that the references listed on the PTO-1449 Form be considered by the Examiner and made of record.

In accordance with 37 C.F.R. § 1.97, this Information Disclosure Statement is not to be construed as a representation that a search has been made, as an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b), or as a representation that no other possibly material information, as defined in 37 C.F.R. § 1.56(b), exists.

Furthermore, the references listed on the attached PTO-1449 Form are not to be construed as an admission that these references qualify as prior art as to the above-referenced application or any related application. Rather, these references are being presented for the Examiner's consideration without prejudice to Applicants' right to demonstrate that any of these references do not qualify as prior art should the Examiner choose to apply any of these references.

The following information is listed below in accordance with 37 C.F.R. §1.98. Any explanation of non-English language documents contained in this Information Disclosure Statement is believed to constitute a concise explanation of the relevance of the listed reference as it is presently understood by the individual designated in § 1.56(c) most knowledgeable about the content of the listed reference, in accordance with 37 C.F.R. § 1.98(a)(3).

Respectfully submitted,

Date: April 1, 2004

Robert A. Manware

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Form PTO-1449 (modified)	ATTY. DOCKET NO.	SERIAL NO.				
	MICS:0103 FLE/MAN (02-1327)	Unassigned				
List of Patents and Publications For Applicant's Information	APPLICANT	APPLICANT				
Disclosure Statement	Jeffery W. Janzen et al.	Jeffery W. Janzen et al.				
(Use several sheets if necessary)	FILING DATE	GROUP				
<u> </u>	Herewith	Unassigned				

U.S. PATENT DOCUMENTS

EXAM. INIT.	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	*						
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FOREIGN PATENT DOCUMENTS

EXAM. INIT.	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION

OTHER ART

(Author, Title, Journal, Volume, Pertinent Pages, & Date) Authors: Jeffery W. Janzen, Scott Schaefer, Todd D. Farrell; Title: Techniques for Storing Accurate Operating Current Values; Serial No.: Unassigned, Filed: 4/1/04. Authors: Jeffery W. Janzen, Scott Schaefer, Todd D. Farrell; Title: Techniques for Implementing Accurate Operating Current Values Stored in a Database; Serial No.: Unassigned, Filed: 4/1/04. EXAMINER DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Information Disclosure Statement--PTO-1449 (Modified)